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To: U.S. Patent and Trademark Office
Examiner Herng-der Day Fax: (571) 273-8300

From: Daniel E. Venglarik, Esq. Time:

Date: December 17, 2007 Client/Matter: 99-B-186
(STMI01-99186)

THIS FAX CONSISTS OF 17 PAGE(S) (INCLUDING THIS COVER SHEET).
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U.S. Serial No. 09/591,621 filed June 9, 2000

Art Unit 2128

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Vidyabhusan Gupta
Serial No.: 09/591,621
Filed: June 9, 2000
For: SYSTEM AND METHOD FOR DESIGNING AND
OPTIMIZING THE MEMORY OF AN EMBEDDED
PROCESSING SYSTEM
Group No.: 2128
Examiner: Heng-der Day

MAIL STOP AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF FACSIMILE TRANSMISSION

The undersigned hereby certifies that the following documents:

- 1) Amendment and Response to Final Office Action

relating to the above application was faxed to 571-273-8300 on December 17, 2007.

Date: 12/17/07

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Sir:

AMENDMENT AND RESPONSE TO FINAL OFFICE ACTION

No fees are believed to be necessary; however, in the event that any fees are required for the prosecution of this application, please charge any necessary fees to Deposit Account No. 50-0208. No extension of time is believed to be necessary. If, however, an extension of time is needed, the extension is requested and please charge the fee for this extension to Deposit Account No. 50-0208.

In response to the Office Action dated October 15, 2007, please amend the above-identified patent application as follows.